***EE/CprE/SE 491 WEEKLY REPORT 02***

***January 31, 2022 – February 4, 2022***

***Group number: SDDEC22\_20***

***Project title: Implement the i281 CPU in Hardware***

***Client &/Advisor:*** Alexander Stoytchev

***Team Members/Role:***

Alex Kiefer

Saffron Edwards

Patrick O'Brien

David Vachlon

Joseph De Jong

o **Weekly Summary**

* *Weekly Objective: Create a hand schematic of the i281 CPU*
* *Incomplete Tasks*

| *Task Name* | *Members on Task* | *Deadline* |
| --- | --- | --- |
| *Pick Group Roles* | *All* | *Feb 08* |
| *Schematic of CPU* | *All* | *Throughout* |
| *Buy Ben Eater Clock* | *Joe* | *Feb 10* |
| *Acquire 6502 Chip* | *Pat* | *Feb 10* |
| *Collect Parts for Clock Assembly* | *Joe, Pat, Alex* | *Feb 10* |
| *Research How to Upload a file to CPU Memory* | *David, Saffron* | *Feb 10* |
| *Research Bus Multiplexer vs Tri-State Drivers* |  | *Feb 17* |

* *Completed Tasks*

| *Task Name* | *Members on Task* | *Deadline* |
| --- | --- | --- |
| *First Schematic of i281 CPU* | *All* | *Feb 1* |
|  |  |  |
|  |  |  |

o **Past week accomplishments**

* Team: We worked as a group to create a schematic of the i281 CPU. We generated four questions and discussed each in-depth with our client. We assigned tasks for each member to complete by Friday of next week.
* Alex Kiefer
	+ Drew up schematic
	+ Planned clock design for CPU
* Saffron Edwards
	+ Collected software files from client
* Patrick O'Brien
	+ Tested i281 compilation in Cadence
* David Vachlon
	+ Research on USB connection from PC to i281 CPU
	+ Generated questions for software size of project
* Joseph De Jong
	+ Organized google drive
	+ Created weekly reports

∙

o **Pending issues**

* Team:
* Alex Kiefer
* Saffron Edwards
* Patrick O'Brien
* David Vachlon
* Joseph De Jong

∙ …

o **Individual contributions** *(Creating this section is optional, but it is* ***Required to include the “Hours Worked for the Week” and their “Total Cumulative Hours” for the project for each member somewhere relevant in your report. Your individual weekly hours should be at a minimum of 6-8 hours for this course. So please manage your time well. Also, ensure that individual contributions support your claim to the weekly hours. Be honest with the reports.****)*

| **NAME**  | **Individual Contributions** *(Quick list of contributions. This should be short.)* | **Hours this** **week** | **HOURS** **cumulative** |
| --- | --- | --- | --- |
| Alex Kiefer  | Schematic, Clock Design | 5 | 11 |
| Saffron Edwards | Software Files | 5 | 11 |
| Patrick O'Brien | Cadence Compliance | 5 | 11 |
| David Vachlon | Connection Types | 5 | 11 |
| Joseph De Jong | Communication, Reports | 5 | 11 |

o **Comments and extended discussion**

* *No extra comments*

o **Plans for the upcoming week**

* *Incomplete Tasks*

| *Task Name* | *Members on Task* | *Deadline* |
| --- | --- | --- |
| *Schematic of CPU* | *All* | *Throughout* |
| *Buy Ben Eater Clock* | *Joe* | *Feb 10* |
| *Acquire 6502 Chip* | *Pat* | *Feb 10* |
| *Collect Parts for Clock Assembly* | *Joe, Pat, Alex* | *Feb 10* |
| *Research How to Upload a file to CPU Memory* | *David, Saffron* | *Feb 10* |
| *Research Bus Multiplexer vs Tri-State Drivers* |  | *Feb 17* |

o **Summary of weekly advisor meeting:** Refer to weekly “Meeting Minutes”